

Gilles Jacquemod Curriculum Vitae

Gilles Jacquemod : Gilles.Jacquemod@unice.fr

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Education and Professional experience

1986: Engineer from ICPI (CPE) Lyon and MSc degree (DEA) in microelectronics from Ecole Centrale Lyon (France)

1989: Ph.D. in Integrated Electronics from INSA Lyon (France)

1989-1990: Attaché Temporaire d'Enseignement et de Recherche (Temporary worker) INSA de Lyon

1990-2000: Associate Professor - Ecole Centrale Lyon (France)

Since 2000: Full Professor University Côte d'Azur - Polytech Nice Sophia (France) and Director of Polytech'Lab, UPR UniCA 7498

Publications

International Journals	65
Book Chapters	2
National Journals	7
International conferences	186
Invited papers, keynotes in International conferences	36
National conferences	58
Communications liées à l'enseignement	30
Press (paper or television newspapers)	21
Patents	4
Total	409

Awards

- Award (35 k€) of Doyen Lépine committee – City of in 2004
- Best Poster Award : SAME 2007 and ICECS 2009
- Second European Award, e-Legacy «Investment in Education» sponsored by Electronics KTN magazine
- Best Demo Award : SAME 2009, 2010, 2011, 2012 and 2014
- Honorable mention recipient of the student paper contest : APS 2011
- Best Paper Award : LAPC 2012 + Best student paper 3rd Place : LAPC 2013
- “Chevalier de l'Ordre National du Mérite” by the French Minister of Higher Education and Research in 2014
- Academic Best Paper Award : CDNLive 2015
- Winner of the Energy-Environment-Climate Public Research Trophy, awarded by ADEME and the journals EnergiePlus and Mesures, in 2015
- Best Paper (Bronze Leaf) : PRIME in 2019
- Keynote Speech Certificate of Honor : ASICON in 2023

Main research topics

- Analog and mixed signal behavioral modelling for IoT and sensor network application
- Low power consumption using advanced technologies (FDSOI)
- Analog and RF circuits design

- CEM (Compatibility Electro-Magnetic: EMI and EMS) on Microcontroller (STM 32)
- Analog Artificial Neural Network (Artificial Intelligence)

I have supervised 36 theses, 33 of which were defended, and I am a reviewer of 17 international journals and of numerous international conferences, member of the organizing committee of several international conferences and chairman of numerous sessions. I am regularly invited as a speaker (invited paper) or during keynotes at international conferences.

Teaching

I am full professor in the electronics department of Polytech Nice Sophia and I am visiting professor at Tianjin University in China since 2014, given 30h lectures on analog CMOS design.

Taught subject	Type	Level	Period	Number_hours/Student
Microelectronics	Lecture	Elec4	2002 → 2022	15h
	Lecture	Elec3	Since 2023	12h
Filtering et Analog modulation	Lecture	Elec4	Depuis 2002	14h
	Exercise			32h
Non Linear electronics and RF	Lecture	Elec4	Since 2003	14h
	Exercise			32h
Analog CMOS	Lecture	Elec4	Since 2000	15h
Sensor network	Lecture	Bat4	2012 → 2018	22,5h
Mixed design	Lecture	Elec5	Since 2000	9h
RF Microelectronics	Lecture	Elec5	Since 2000	21,5
Analog Electronics	Lab + Lecture Lab	PEIP2	Since 2015	12h +
			Since 2015	4,5h
			Since 2021	16h
Low voltage design (g_m/I_D method)	Lecture	Elec5	Since 2020	6h
Analog Electronics	Lecture	M1Tianjin	2014 → 2020	30h

Remark: PEIP2 (equivalent of L2), Elec4 (or Bat4 : equivalent M1) and Elec5 (equivalent of M2)